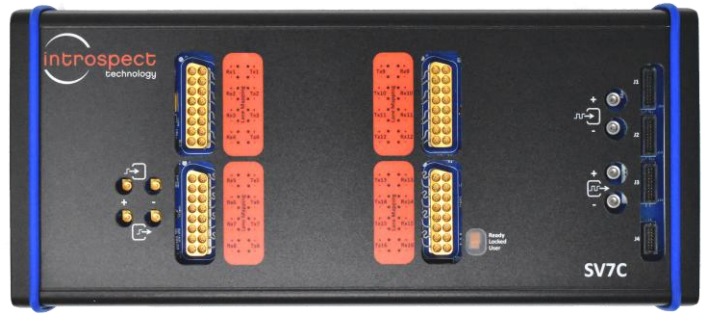


C SERIES

SV7C-eDP

Embedded DisplayPort Analyzer



Complete Analysis of eDP up to v2.0 and DP up to v2.1 Systems in HBR and UHBR

The SV7C-eDP Embedded DisplayPort Analyzer is an ultra-portable, high-performance instrument capable of analyzing traffic for Embedded DisplayPort and DisplayPort applications. The SV7C-eDP Analyzer may be used as either a DisplayPort sink device (for source testing) or as a probing solution for capturing protocol traffic on an Embedded DisplayPort bus. The Analyzer detects display parameters, supports link training, captures live traffic, and extracts full video frames. All these functions are integrated into the award-winning Pinetree™ software environment. Figure 1 below illustrates a typical application of the SV7C-eDP Analyzer connected to an Embedded DisplayPort Source.

KEY FEATURES:

- **Protocols:** supports Embedded DisplayPort (eDP) up to v2.0 and DisplayPort (DP) up to v2.1 in HBR and UHBR
- **Supported Data Rates:** up to 26 Gbps, including bus probing applications with Introspect PV2 active probes
- **Lane Count:** configurable from 1 to 4 lanes (ML1 to ML4) plus auxiliary channel (AUX)

KEY BENEFITS:

- **Data Capture:** extensive triggering functions and consecutive frame capture capabilities
- **Diagnostics:** full video frame extraction and data analysis tools
- **Self-Contained:** an all-in-one system that enables the simplest bench environment for physical layer test to full protocol layer validation

Typical Application: Testing a DisplayPort Design with the SV7C-eDP Analyzer Emulating a Sink Device

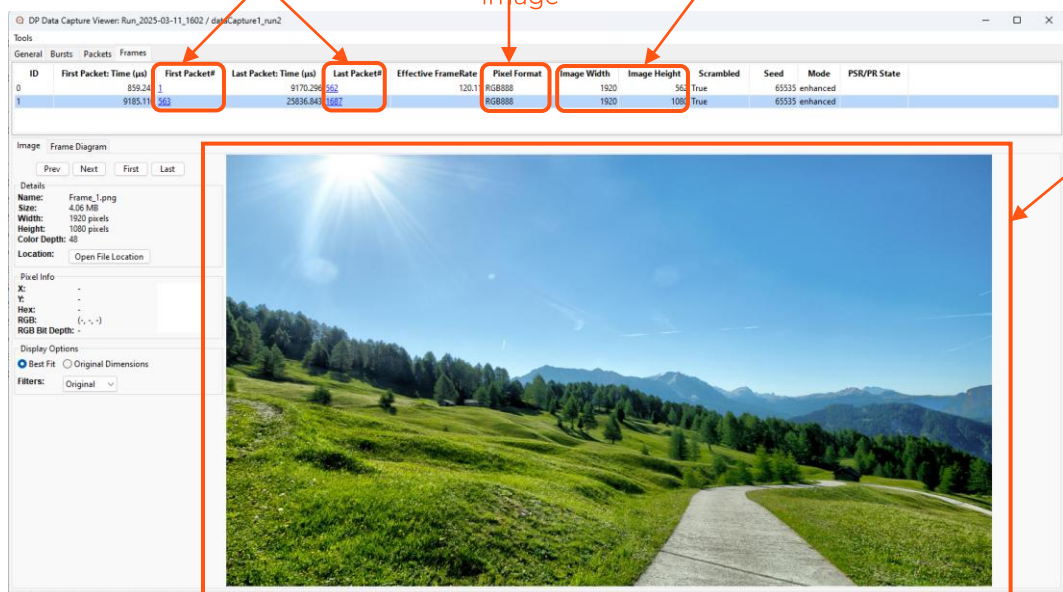


The SV7C-eDP Analyzer Connected to a DisplayPort System

General Specifications

PARAMETER	VALUE	UNITS	DESCRIPTION
Supported Protocols			
Physical Layer Interface	eDP v2.0 DP v2.1		Supports HBR and UHBR
Ports			
Number of analyzer lanes	5		ML 1 to ML 4 AUX Channel (bidirectional)
Number of GPIO pins	30		Programmable as external trigger input or flag output pins
Number of dedicated reference clock inputs	1		
Number of dedicated reference clock outputs	1		
PC Connections for Pinetree Control	2		USB2 and USB Type-C
Data Rates and Reference Clocks			
Maximum Data Rate	26	Gbps	Per Lane
Maximum External Input Reference Clock	250	MHz	
Maximum External Output Reference Clock	500	MHz	

The SV7C-eDP Analyzer Supports Single Stream (SST) and Multi-Stream Transport (MST) and with up to Four Virtual Channels



Hyperlinks to first & last packet of the frame in the packets tab

Format of the captured image

Frame dimensions

Reconstructed image

Pinetree View of Capture Results on the Frames Tab

TO LEARN MORE, SEND US AN EMAIL AT [INFO@INTROSPECT.CA](mailto:info@introspect.ca)